VEE 2016

12th ACM SIGPLAN/SIGOPS international conference on

Virtual Execution Environments

Atlanta, GA April 2-3, 2016 with ASPLOS

Authors are invited to submit original papers related to virtualization across all layers of the software stack, from high-level language virtual machines down to the microarchitectural level. VEE 2016 accepts both full-length and short papers.

Abstract deadline: November 23, 2015

Paper deadline: November 30, 2015



General Chair

Vishakha Gupta-Cledat (Intel Labs)

Program Co-chairs

Donald Porter (Stony Brook University) Vivek Sarkar (Rice University)

Program Committee

Jonathan Appavoo Boston University

Tzi-Cker Chieuh Industrial Technology Research Institute,

Taiwar

John Criswell University of Rochester
Dilma Da Silva Texas A&M University

Julian Dolby Björn Franke Soo-Mook Moon Guilherme Ottoni Kevin Pedretti

Behnam Robatmili Chris Rossbach

Mark Silberstein Mary Lou Soffa Malgorzata Steinder Priyanka Tembey Peng Wu

IBM Thomas J. Watson Research Center University of Edinburgh Seoul National University

Facebook

Sandia National Laboratories

Qualcomm Research

VMware Research and University of

Texas at Austin

Technion---Israel Institute of Technology

University of Virginia IBM Research

VMware

Huawei America Lab



in cooperation with



http://conf.researchr.org/home/vee-2016

VEE 2016

12th ACM SIGPLAN/SIGOPS international conference on

Virtual Execution Environments

Atlanta, GA April 2-3, 2016 with ASPLOS

The 12th ACM SIGPLAN/SIGOPS International Conference on Virtual Execution Environments (VEE'16) brings together researchers and practitioners from different computer systems domains to interact and share ideas in order to advance the state of the art of virtualization and broaden its applicability. VEE'16 accepts both full-length and short papers. Both types of submissions are reviewed to the same standards and differ primarily in the scope of the ideas expressed. Short papers are limited to half the space of full-length papers. The program committee will not accept a full paper on the condition that it is cut down to fit in a short paper slot, nor will it invite short papers to be extended to full length. Submissions will be considered only in the category in which they are submitted.

We invite authors to submit original papers related to virtualization across all layers of the software stack down to the microarchitectural level. Topics of interest include (but are not limited to):

- virtualization support for programs and programmers;
- architecture support for virtualization;
- operating system support for virtualization;
- compiler and programming language support for virtualization;
- runtime system support for virtualization;
- virtual I/O, storage, and networking;
- memory management;
- managed runtimes and virtual machines;
- management technologies for virtual environments;
- performance analysis and debugging for virtual environments;
- security and virtual environments
- virtualization in cloud computing
- virtualization technologies applied to specific problem domains such as HPC, realtime, and power management.

Abstract deadline Monday, November 23, 2015 (11:59PM EST)
Full paper deadline Monday, November 30, 2015 (11:59pm, EST)
Author response period Tuesday-Wednesday, January 26-27, 2016

Author notification Friday, February 5, 2016

http://conf.researchr.org/home/vee-2016